2SD1423, 2SD1423A

Silicon NPN epitaxial planar type

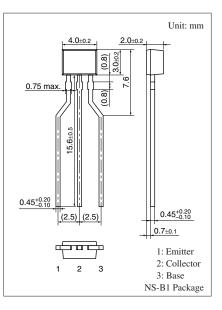
For low-frequency amplification Complementary to 2SB1030 and 2SB1030A

Features

- Optimum for high-density mounting
- Allowing supply with the radial taping

Parameter	Symbol	Rating	Unit	
Collector-base voltage	2SD1423	V _{CBO}	30	V
(Emitter open)	2SD1423A		60	
Collector-emitter voltage	2SD1423	V _{CEO}	25	V
(Base open)	2SD1423A		50	
Emitter-base voltage (Coll	V _{EBO}	7	V	
Collector current	I _C	0.5	А	
Peak collector current	I _{CP}	1	А	
Collector power dissipatio	P _C	300	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	





Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

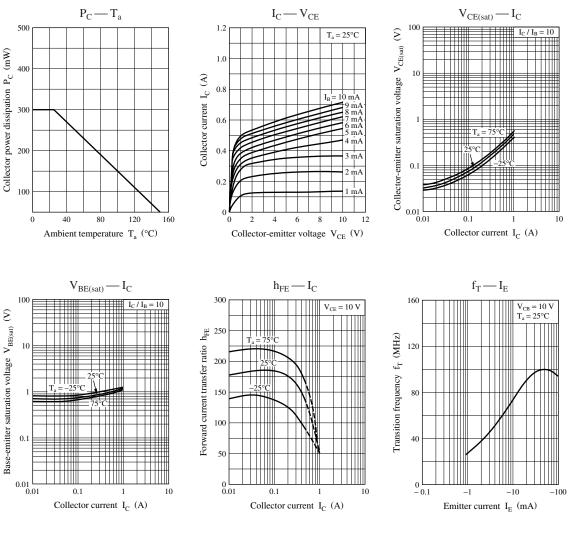
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage	2SD1423	V _{CBO}	$I_{C} = 10 \ \mu A, \ I_{E} = 0$	30			V
(Emitter open)	2SD1423A			60			
Collector-emitter voltage	2SD1423	V _{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	25			V
(Base open)	2SD1423A			50			
Emitter-base voltage (Collector open)		V _{EBO}	$I_E = 10 \ \mu A, \ I_C = 0$	7			V
Collector-base cutoff current (Emitter open)		I _{CBO}	$V_{CB} = 20 V, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)		I _{CEO}	$V_{CE} = 20 \text{ V}, I_B = 0$			1	μΑ
Forward current transfer ratio		h _{FE1} *	$V_{CE} = 10 \text{ V}, I_C = 150 \text{ mA}$	85		340	_
		h _{FE2}	$V_{CE} = 10 \text{ V}, I_C = 500 \text{ mA}$	40			
Collector-emitter saturation voltage		V _{CE(sat)}	$I_{\rm C} = 300 \text{ mA}, I_{\rm B} = 30 \text{ mA}$			0.6	V
Transition frequency		f _T	$V_{CB} = 10 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		200		MHz
Collector output capacitance		C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		6	15	pF
(Common base, input open circuited)							

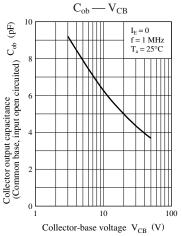
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *: Rank classification

Rank	Q	R	S	
$h_{\rm FE1}$	85 to 170	120 to 240	170 to 340	

Panasonic





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